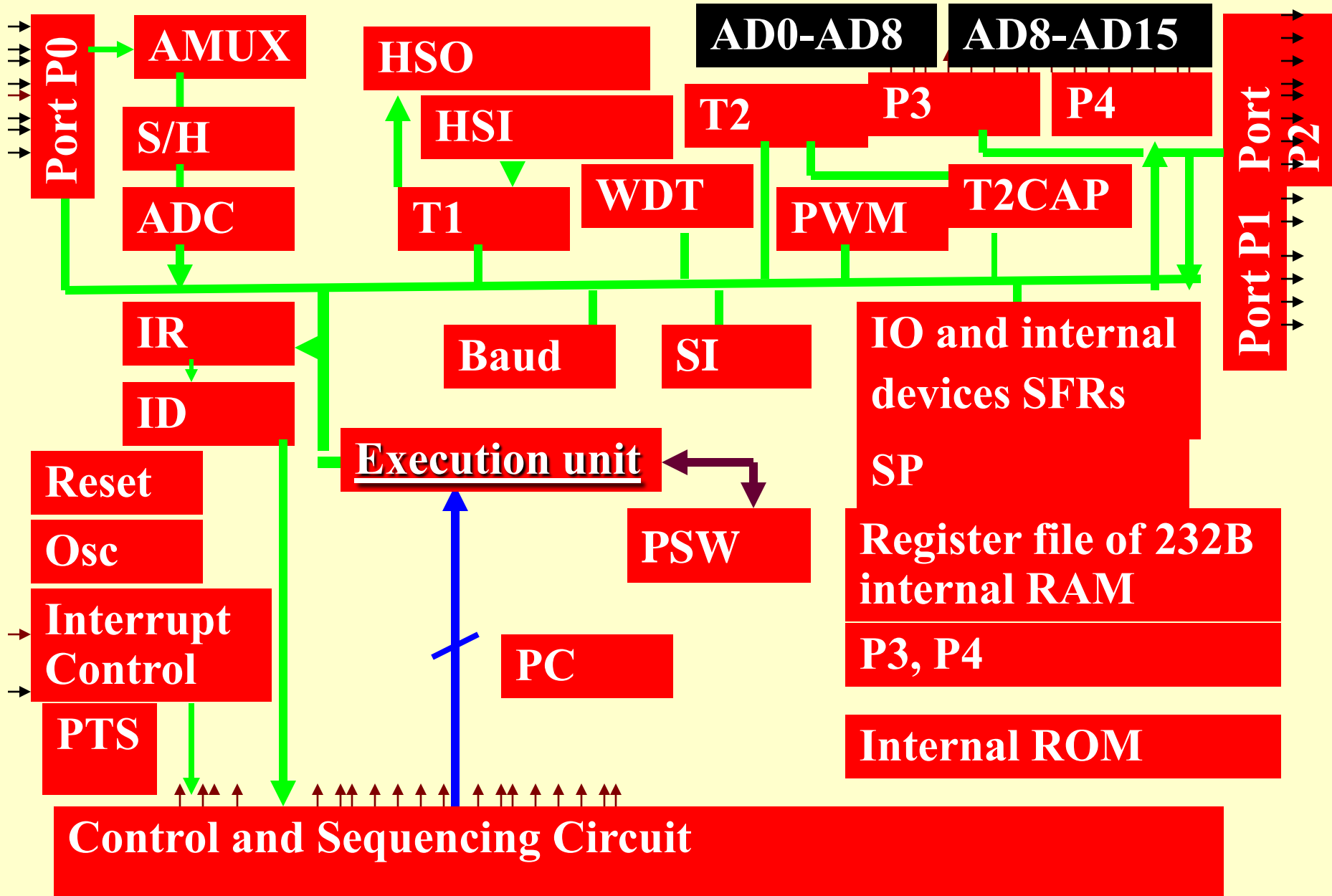


Chapter 14

80x96 Family Microcontrollers



Lesson 5

IO Ports

Port P0

**Address
-OEH**

P0.7	I
P0.6	I
P0.5	I
P0.4	I
P0.3	I
P0.2	I
P0.1	I
P0.0	I

Option 1

Port P0

**Address
-0EH**

P0.7

I

P0.6

I

P0.5

I

P0.4

I

P0.3

I

P0.2

I

P0.1

I

P0.0

I

**Analog
Input
Option 2**

AN7

AN6

AN5

AN4

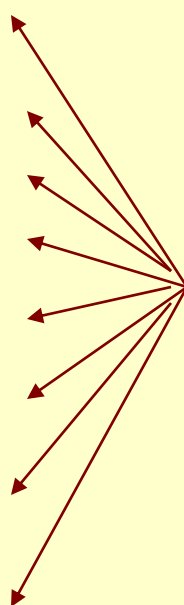
AN3

AN2

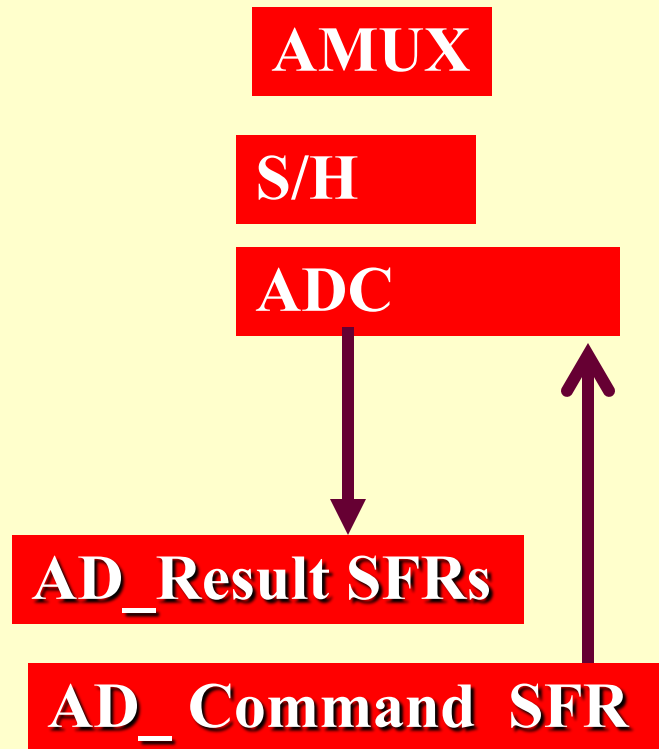
AN1

AN0

**Analog
Inputs**

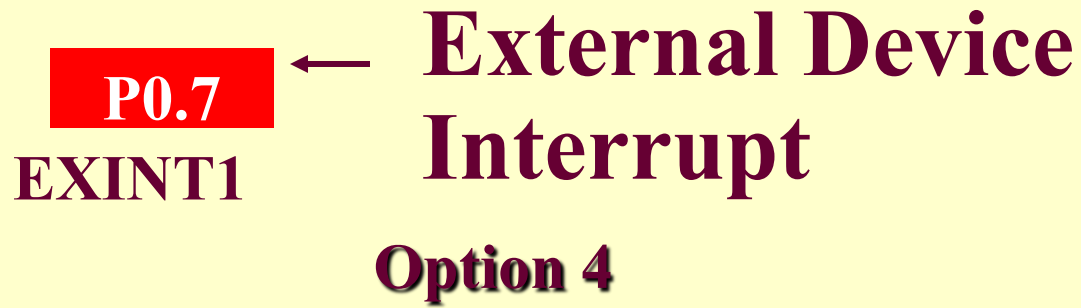


Port P0 Address -0EH



Option 3

Port P0



Port P1 Address – 0FH

P1.7	IO
P1.6	IO
P1.5	IO
P1.4	IO
P1.3	IO
P1.2	IO
P1.1	IO
P1.0	IO

Option 1

Port P1 Address – 0FH

P1.7	IO
P1.6	IO
P1.5	IO
P1.4	→ PWM1
P1.3	→ PWM2
P1.2	IO
P1.1	IO
P1.0	IO

Option 2

Port P1

Address – 0FH

P1.7
P1.6
P1.5
P1.4
P1.3
P1.2
P1.1
P1.0

Hold
HLDA ↘
 ↗ PTS

IO
IO or PWM1
IO or PWM2
IO
IO
IO

PTS:
Peripheral
Transactions
Server
function as
DMA

Option 3

Address – 10H Port P2

P2.7	IO
P2.6	IO
P2.5	IO
P2.4	IO
P2.3	IO
P2.2	IO or EXINT
P2.1	IO
P1.0	IO

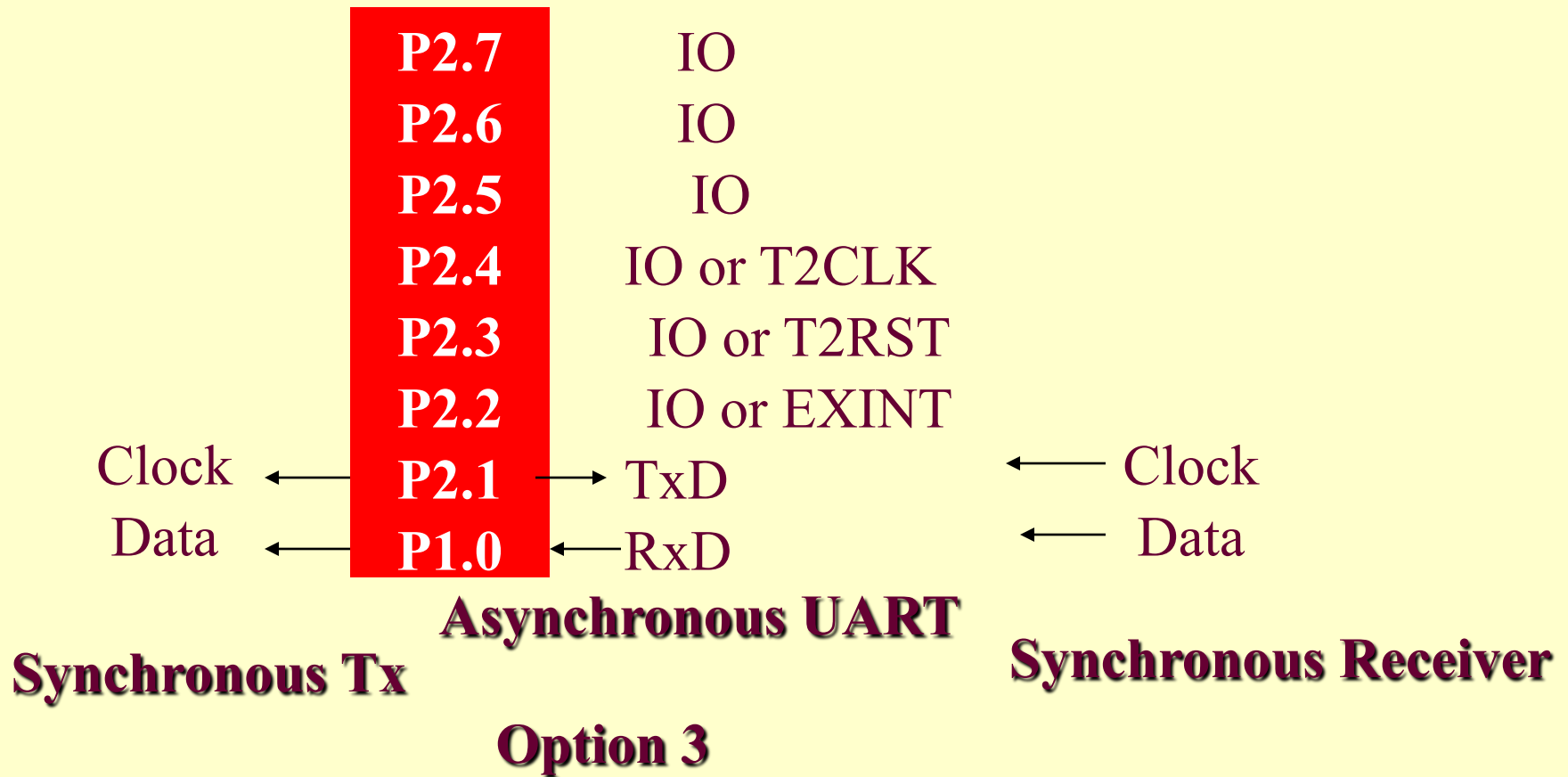
Option 1

Address – 10H Port P2

P2.7	IO
P2.6	IO
P2.5	IO
P2.4	T2CLK
P2.3	T2RST
P2.2	IO or EXINT
P2.1	IO
P1.0	IO

Option 2

Address – 10H Port P2



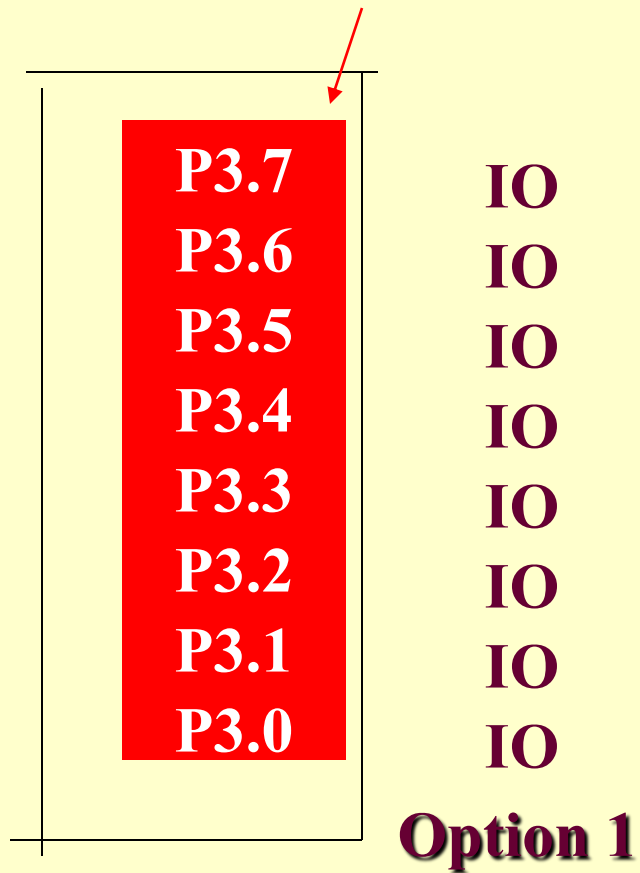
Address – 10H Port P2

P2.7	IO
P2.6	IO
P2.5	PWM0
P2.4	IO or T2CLK
P2.3	IO or T2RST
P2.2	IO or EXINT
P2.1	IO or TxD or Clock
P1.0	IO or RxD or Data

Option 4

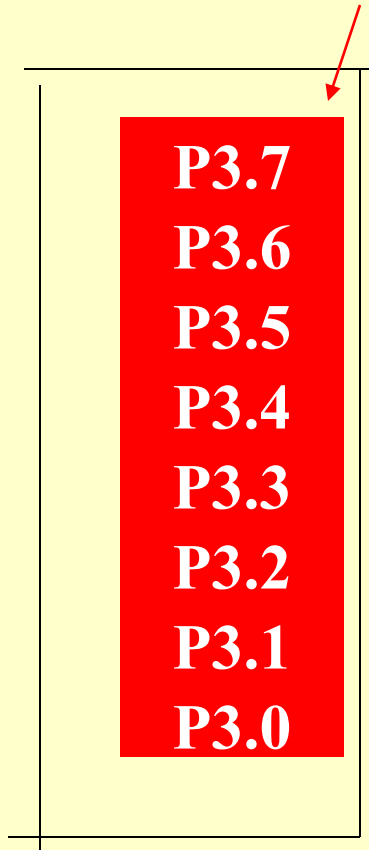
Address – 10FEH

Port P3



**Address
- 10FEH**

Port P3



**Address
bits**

A7

A6

A5

A4

A3

A2

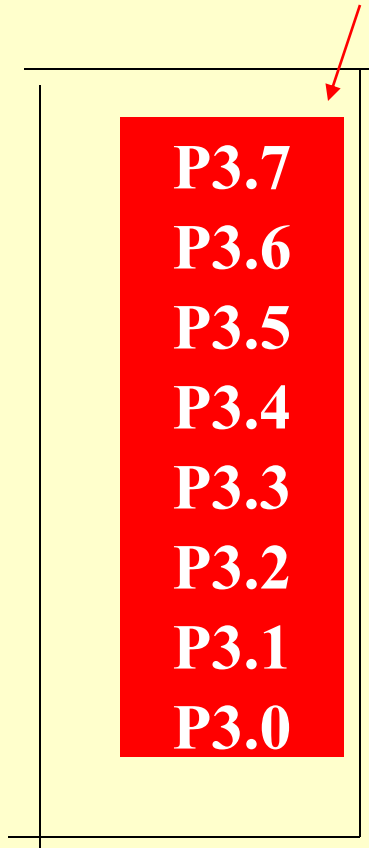
A1

A0

Option 2

**Address
- 10FEH**

Port P3



Data bits

D7

D6

D5

D4

D3

D2

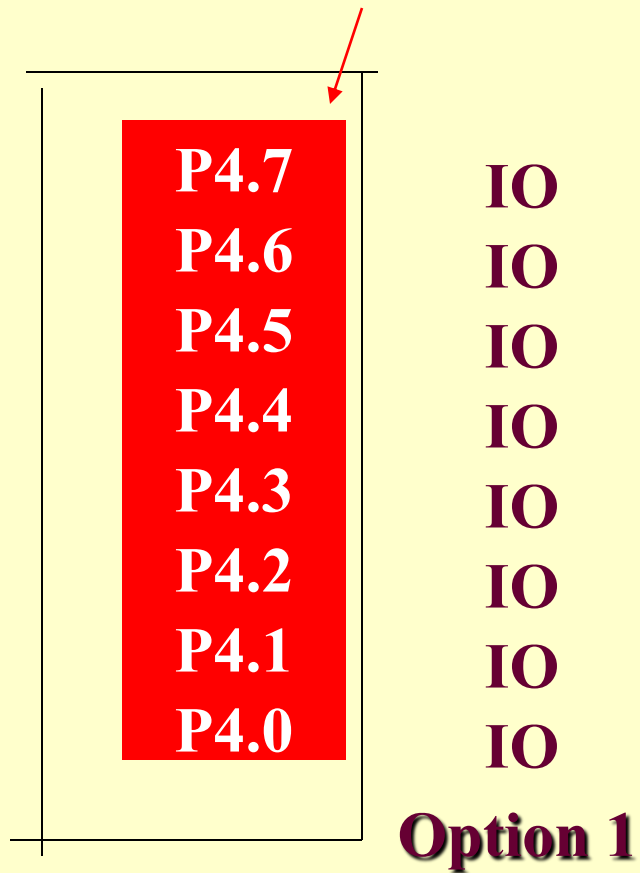
D1

D0

Option 3

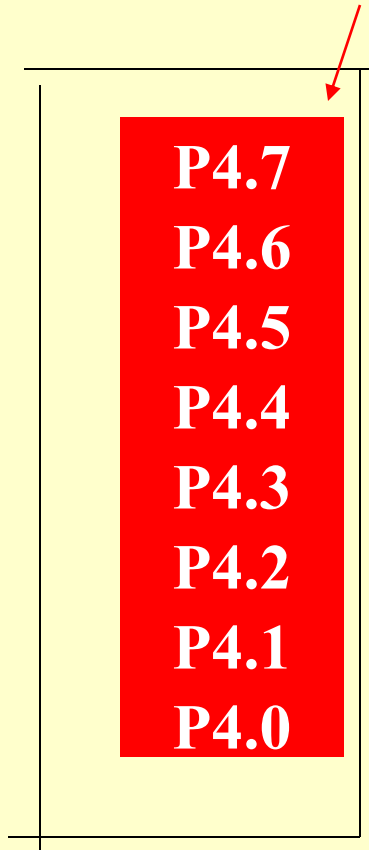
Address – 10FFH

Port P4



**Address
- 10FFH**

Port P4



**Address
bits**

A15

A14

A13

A12

A11

A10

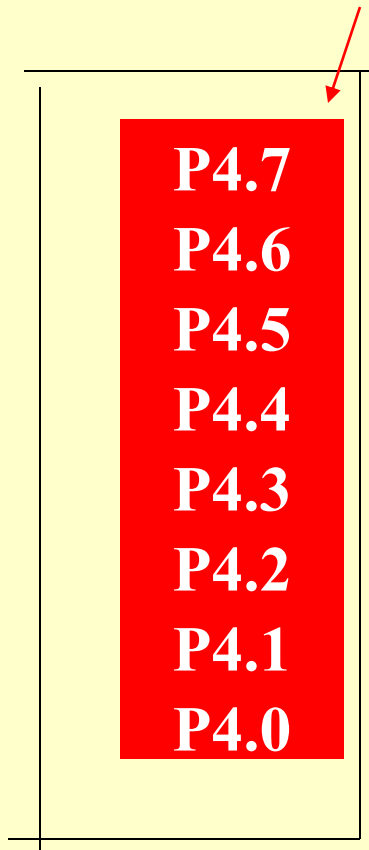
A9

A8

Option 2

**Address
- 10FFH**

Port P4



Data bits

D15

D14

D13

D12

D11

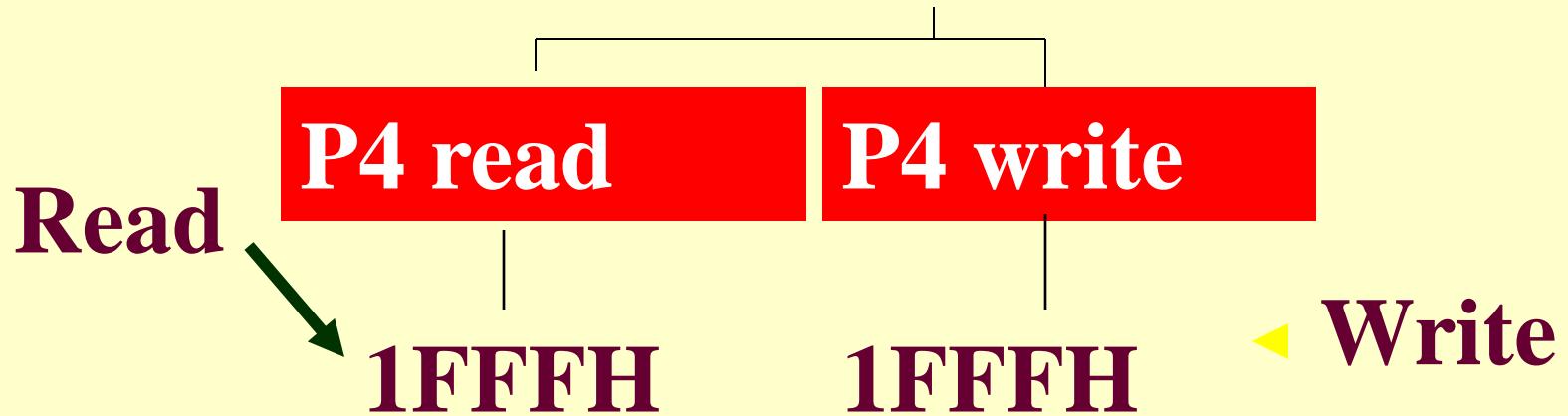
D10

D9

D8

Option 3

P4 Address



Summary

We learnt

- Ports and their Addresses in SFR space

End of Lesson 5 on IO Ports